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 - L1: (2) argument near9 command near9 write near9 burst
 - L2: (4109) argument near9 first
 - L3: (4) 2 near9 command near9 burst
 - L4: (302) address\$4 near9 command near9 write near9 burst
 - L5: (5) (argument parameter) near9 4
 - L6: (11) (argument parameter) near9 command near9 burst near9 write
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DBs: US-PPGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB

Default operator: OR

(argument parameter) near9 command near9 burst near9 write

Plurals

Highlight all hit terms initially

	<input type="checkbox"/> U	<input checked="" type="checkbox"/> Document ID	Issue Date	Pages	Title	Current OR	Current XRef	Retrieval Cls	Inventor	S	C	P	③	④	Im
1	<input type="checkbox"/>	<input type="checkbox"/> US 20060161218	20060720	99	Systems and methods for treati	607/45			Danilov; Yuri Petrovich	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US
2	<input type="checkbox"/>	<input type="checkbox"/> US 20050144372	20050630	18	Memory device controlled with	711/105	711/156		Walker, Robert	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US
3	<input type="checkbox"/>	<input type="checkbox"/> US 20050122329	20050609	9	Burst transfer register arran	345/501			Janus, Scott R.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US
4	<input checked="" type="checkbox"/>	<input type="checkbox"/> US 20050055592	20050310	69	Method for modular design of a	713/322			Velasco, Francisco et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US
5	<input type="checkbox"/>	<input type="checkbox"/> US 6813674 B1	20041102	66	Dual-edge fifo interface	710/311	710/309;		Velasco; Francisco et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US
6	<input type="checkbox"/>	<input type="checkbox"/> US 6115823 A	20000905	66	System and method for task per	713/322	710/16;		Velasco; Francisco et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US
7	<input type="checkbox"/>	<input type="checkbox"/> US 6084803 A	20000704	10	Initialization of non-volatile progr	365/189.05	365/225.7;		Sredanovic; Nikolas et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US
8	<input type="checkbox"/>	<input type="checkbox"/> US 5841731 A	19981124	14	Semiconductor device having	365/233	365/191;		Shinozaki; Naoharu	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US
9	<input type="checkbox"/>	<input type="checkbox"/> US 4726380 A	19880223	38	Implantable cardiac pacer with	607/15	607/16;		Vollmann; William et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US
10	<input type="checkbox"/>	<input type="checkbox"/> US 20050144372	20050630	18	Memory accessing method e.g.				WALKER, R	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US
11	<input type="checkbox"/>	<input type="checkbox"/> US 20050122329	20050609	9	Command burst writing method				JANUS, S R	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US

Hits Details HTML

Ready

NUM: